

Electronic Acknowledgement Receipt

EFS ID:	2621601
Application Number:	10791337
International Application Number:	
Confirmation Number:	2401
Title of Invention:	Spacer-less transistor integration scheme for high-k gate dielectrics and small gate-to-gate spaces applicable to Si, SiGe strained silicon schemes
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Attorney Docket Number:	03-2051/LSI1P240
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Application Type:	Utility under 35 USC 111(a)

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Payment was successfully received in RAM	\$ 120
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Extension of Time	LSI240_Petition_for_Ext.pdf	22984	no	1
			07a1c322c80dd0b2039cf52eb28b33a270d79cc0		

Warnings:**Information:**

2	Fee Worksheet (PTO-06)	fee-info.pdf	8249	no	2
			61d0519f560162d0f093270fb87e03a2faa1c025		

Warnings:**Information:**

Total Files Size (in bytes):			31233
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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